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JCWSCS 2 3 MAR 2004

Sent By: Carr Law Firm, LLP;

214 760 3003 ;

Mar-23-04 2:09PM;

Page 1

CARR LLP

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Facsimile Information Sheet

DATE: **March 23, 2004**

CLIENT/MATTER NO.: **AUS920030713US1**
(IBM 2804000)

TO: **Office of Initial Patent
Examination's Filing Receipt
Corrections**

Certificate of Transmission under 37 CFR 1.8
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on 3-23-04
Kalina Hulshouser
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FIRM NAME: **US Patent & Trademark Office**

CITY, STATE: **Alexandria, Virginia**

FACSIMILE NUMBER: 703-746-9195

TELEPHONE NUMBER: _____

RE: **Request for Correction of Filing Receipt, Application Serial; No. 10/733,840**

TOTAL NUMBER OF PAGES 4 (including Facsimile Information Sheet)

FROM: Gregory W. Carr, Reg. No. 31,093

MESSAGE:

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Attorney Docket No.
AUS920030713US1 (IBM 2804000)

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Alex Chughen Chow

Title: Method For Hiding Latency In A Task-Based Library Framework For A
Multiprocessor Environment

U.S. Serial No.: 10/733,840

Filed: December 11, 2003

Group Art Unit: 2126

Examiner: To be assigned

CERTIFICATE OF TRANSMISSION	
I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Office of Initial Patent Examiner's Filing Receipt Corrections, 703-746-9195, on:	
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Office of Initial Patent Examination's Filing Receipt Corrections
United States Patent and Trademark Office
Alexandria, Virginia 22313

Dear Sir:

REQUEST FOR CORRECTION OF FILING RECEIPT

Applicants hereby request that the Filing Receipt data of the above-identified U.S. patent application as listed on the records of the Patent and Trademark Office be corrected.

Submitted herewith is a copy of the corrected Filing Receipt indicating in red that the following should be changed:

Assignment For Published Patent Application:

Please delete 'Mahcines' and replace with 'Machines'. The correct spelling should be International Business Machines Corporation.

Attorney Docket No.
AUS920030713US1 (IBM 2804000)

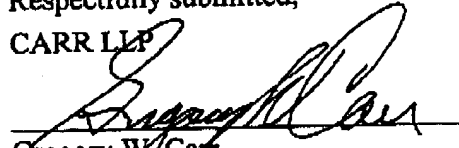
PATENT APPLICATION

Accordingly, it is respectfully requested that the above corrections be made.

It is believed that this request for correction of error does not require the payment of a fee. However, if it should be determined that a fee is required, please charge any required fee during the pendency of this application (other than the issue fee) to Deposit Account No. 50-0605 of CARR LLP.

Respectfully submitted,
CARR LLP

Date: 3/23/04


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/733,840	12/11/2003	2126	892	AUS920030713US1	4	22	4

CONFIRMATION NO. 2185

Gregory W. Carr
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FILING RECEIPT



OC000000012136240

Date Mailed: 03/18/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Alex Chughen Chow, Austin, TX;

Assignment For Published Patent Application

International Business Machines Corporation, Armonk, NY;
Machines

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 03/18/2004

Projected Publication Date: 06/16/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method for hiding latency in a task-based library framework for a multiprocessor environment

Preliminary Class